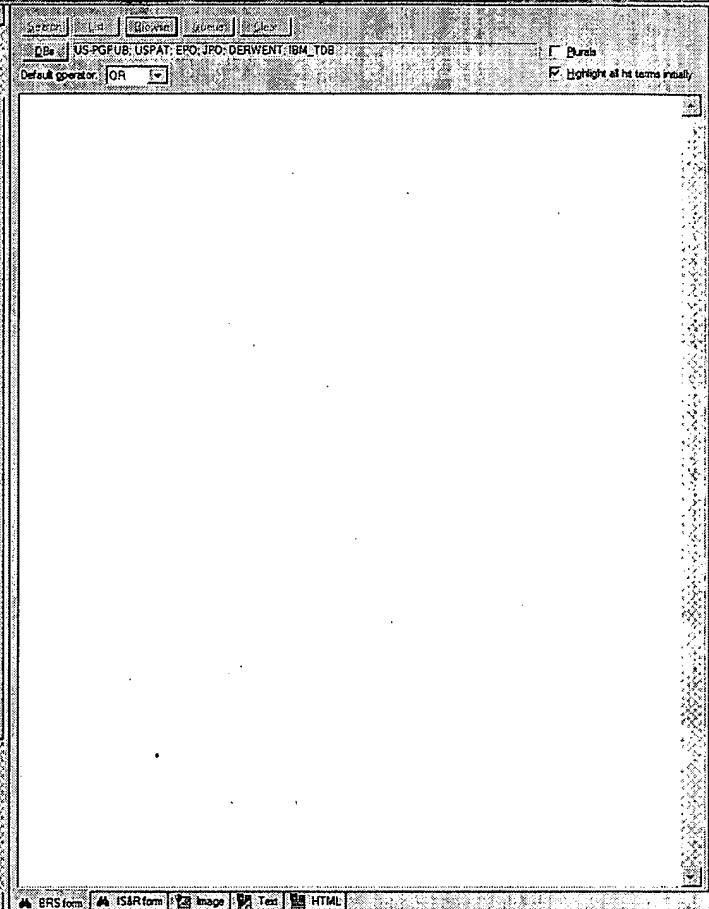


10665401.wsp:1

Active

- ↳ L2: (3287412) (voltage or current)
- ↳ L3: (3860020) circuit\$3
- ↳ L4: (22138) 2 near3 limit\$3 near2 3
- ↳ L5: (3287412) current-to-voltage or (voltage-to-current) or 2
- ↳ L6: (48766) converter\$1 near2 3
- ↳ L7: (692869) buffer
- ↳ L8: (2192845) sens\$3
- ↳ L9: (17388) 5 with 6
- ↳ L10: (2703990) 7 or 8
- ↳ L12: (1) 11 and 1
- ↳ L11: (61) 9 same 10 same 4
- ↳ L13: (22) 11 and memory
- ↳ L14: (6758) current-to-voltage or (voltage-to-current)
- ↳ L15: (2071007) convert\$3
- ↳ L16: (4952) 14 near4 15
- ↳ L17: (21) 16 with 4
- ↳ L18: (3145468) read\$3
- ↳ L19: (1610130) memory
- ↳ L20: (486590) 18 with 19
- ↳ L21: (2) 20 same 4 same 14
- ↳ L22: (4039) current-to-voltage
- ↳ L23: (16) 20 and 22 and 4
- ↳ L24: (154723) 2 near3 limit\$3
- ↳ L25: (2) 24 same 22 same 19 same 18
- ↳ L26: (63592) voltage near3 limit\$3
- ↳ L28: (2) 27 same 19
- ↳ L29: (2) 27 same 19
- ↳ L30: (22) 27 and 19
- ↳ L27: (59) 26 same 22
- ↳ L34: (43492) limit\$3 adj3 voltage\$1
- ↳ L35: (255593) storage adj2 (memor\$3 or cell\$1 or element\$1 or medium)
- ↳ L36: (62) 34 with 35
- ↳ L1: (6) chalcogenide near2 storage near2 element
- ↳ L46: (541) 8 with 2 with 22
- ↳ L47: (2052686) state
- ↳ L48: (73566) phase near2 chang\$3
- ↳ L49: (43) 46 same 47
- ↳ L50: (1) 48 and 49
- ↳ L51: (30) 49 and 19
- ↳ L52: (3018018) detect\$3 or determin\$3
- ↳ L53: (10048) 52 with 48
- ↳ L54: (696) 53 with 47
- ↳ L55: (8) 54 with 35
- ↳ L56: (8) 54 same 35
- ↳ L57: (69) 54 and 35



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